

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: I-Sheng Liu; Shang-De Ted Chang
Title: Source/Drain Adjust Implant
Assignee: Programmable Microelectronics Corporation
Application No.: 10/753,673 Filing Date: January 7, 2004
Examiner: Johannes P. Mondt Group Art Unit: 2826
Docket No.: M-15281 US Confirmation No.: 6785

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CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following documents are being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below:

1) Response to Election/Restriction Requirement (1 page).

Dated: January 20, 2005


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Number of pages (including this sheet): 2

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RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Dear Sir:

In response to the Restriction Requirement dated December 20, 2004, Applicants hereby elect without traverse to prosecute Invention I, Claims 1-9, drawn to a two-transistor PMOS memory cell. Applicants reserve the right to prosecute non-elected Claims 10-14 in a divisional application.

If the Examiner has any questions, please call the undersigned at (949) 752-7040.

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Linda Bolter

January 20, 2005
 Date of Signature

Respectfully submitted,

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